

Enhanced Test Condition

PSE Conformance Test Suite					802.3at Conformance Report					
February 12 2025		1:55 PM			version 5.3.14					
Port Count.....		1			Test Mode: 30 Watt PHY			report version 5.3.08		
Loop Count.....		1			Sifos Interop Index*: 100%					
PSE Tested: Unspecified Type-2 30W			Error Log: None							
Chassis ID: 192.168.221.105		PSA-3000 Ports					Low	P/F	High	P/F
TestLoop: 1		1-1	UNITS	Min	Max	Average	Limit		Limit	
Test: det_v										
Open_Circuit_Det_Voc=		14.45	V	14.45	14.45	14.45	2.8	Pass	30	Pass
Peak_Det_Vvalid=		6.53	V	6.53	6.53	6.5	3.8	Pass	10	Pass
Min_Det_Vvalid=		4.21	V	4.21	4.21	4.2	2.8	Pass	9	Pass
Det_Volt_Step_dVtest=		1.82	V	1.82	1.82	1.8	1	Pass	7.2	Pass
Detection_Slew=		0	V/usec	0	0	0	0	Pass	0.1	Pass
Good_Sig_Det_Pulse=		7	edges	7	7	7	1	Pass	9	Pass
Backoff_Voltage=		0.4	V	0.4	0.4	0.4	0	Pass	2.8	Pass
Non_802_Discr_?=		0	****	0	0	0	0	Pass	0	Pass
Detect_Strategy=		1	****	1	1	1	0	Pass	2	Pass
Test: det_i										
Init_Current_Isc=		0.16	mA	0.16	0.16	0.16	0	Pass	5	Pass
Det_Current_Isc=		0.15	mA	0.15	0.15	0.15	0	Pass	5	Pass
Test: det_range										
Rgood_Max=		30	Kohm	30	30	30	26	Pass	32	Pass
Rgood_Min=		17	Kohm	17	17	17	16	Pass	19	Pass
Rmid_det=		30	Kohm	30	30	30	26	Pass	33	Pass
Cgood_Max=		0.1	uF	0.1	0.1	0.1	0	Pass	10	Pass
Rbad_Cbad_Stat=		0	****	0	0	0	0	Pass	0	Pass
4Pair_Pwr_?=		0	****	0	0	0	0	Pass	0	Pass
Test: det_time										
Backoff_Time_Tdbo=		1035	msec	1035	1035	1035	-1	Pass	1500	Pass
Eff_Backoff_Tdbo_eff=		1035	msec	1035	1035	1035	-1	Pass	1500	Pass
Backoff_Type=		0	****	0	0	0	0	Pass	0	Pass
Detection_Time_Tdet=		373	msec	373	373	373	5	Pass	500	Pass
Total_Det_Time=		383	msec	383	383	383	5	Pass	1000	Pass
Test: det_rsource										

Enhanced Test Condition

PSE_Detect_Source=	1	****	1	1	1	0	Pass	1	Pass
Output_Impedance_Zout=	450	Kohm	450	450	450	45	Pass	2000	Pass
Test: class_v									
Vclass_Max=	18.6	V	18.6	18.6	18.6	15.5	Pass	20.5	Pass
Vclass_Min=	18.7	V	18.7	18.7	18.7	15.5	Pass	20.5	Pass
Mark_Voltage_Vmark=	8.7	V	8.7	8.7	8.7	7	Pass	10	Pass
Mark_Voltage_Min=	8.7	V	8.7	8.7	8.7	7	Pass	10	Pass
Test: class_time									
Class_0_Count=	1	****	1	1	1	1	Pass	2	Pass
Class_Time_Tpdc=	12.7	msec	12.7	12.7	12.7	6	Pass	75	Pass
Class_4_Count=	2	****	2	2	2	2	Pass	2	Pass
Event1_Tcle1=	11.7	msec	11.7	11.7	11.7	6	Pass	30	Pass
Event2_Tcle2=	11.7	msec	11.7	11.7	11.7	6	Pass	30	Pass
Mark_Tme1=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Mark_Tme2=	253.1	msec	253.1	253.1	253.1	6	Pass	376	Pass
Mark_Tme1_LowI=	11.7	msec	11.7	11.7	11.7	6	Pass	12	Pass
Test: class_err									
Class_lim=	69	mA	69	69	69	51	Pass	100	Pass
Pwr_Cl_lim=	0	****	0	0	0	0	Pass	0	Pass
Pwr_Cl_55=	0	****	0	0	0	0	Pass	0	Pass
Mark_lim=	6	mA	6	6	6	5	Pass	100	Pass
Pwr_Cl_Uneven=	0	****	0	0	0	0	Pass	0	Pass
Treset=	211	msec	211	211	211	15	Pass	10000	Pass
Test: pwrup_time									
Power-On_Trise_c0=	150	usec	150	150	150	15	Pass	50000	Pass
Power-On_Tpon_c0=	271.5	msec	271.5	271.5	271.5	0	Pass	400	Pass
Power-On_Trise_c4=	165	usec	165	165	165	15	Pass	50000	Pass
Power-On_Tpon_c4=	294.9	msec	294.9	294.9	294.9	0	Pass	400	Pass
Test: pwrup_inrush									
Init_Iinrush=	446	mA	446	446	446	400	Pass	450	Pass
Max_Iinrush_c0=	427.5	mA	427.5	427.5	427.5	400	Pass	450	Pass
Max_Iinrush_c4=	427.38	mA	427.38	427.38	427.4	400	Pass	450	Pass
Min_Iinrush=	416	mA	416	416	416	400	Pass	450	Pass
Tinrush=	60	msec	60	60	60	50	Pass	75	Pass
Inrush_45m=	55.7	V	55.7	55.7	55.7	50	Pass	57	Pass

Enhanced Test Condition

Inrush_Voltage=	30.7	V	30.7	30.7	30.7	30	Pass	57	Pass
Inrush_Strategy_c0=	0	****	0	0	0	0	Pass	1	Pass
Inrush_Strategy_c4=	0	****	0	0	0	0	Pass	1	Pass
Test: pwr_on_v									
Vport_min=	55.1	V	55.1	55.1	55.1	50	Pass	57	Pass
Vport_max=	55.8	V	55.8	55.8	55.8	50	Pass	57	Pass
Vport_ripple=	13	mVpp	13	13	13	0	Pass	500	Pass
Vport_noise=	30	mVpp	30	30	30	0	Pass	200	Pass
Vtrans_min=	55	V	55	55	55	50	Pass	57	Pass
Vtrans_max=	55.8	V	55.8	55.8	55.8	50	Pass	57	Pass
Test: pwr_on_pwr_cap									
Pcon_c0=	18.4	W	18.4	18.4	18.4	14.2	Pass	22.7	Pass
Icon_%_c0=	134.5	%	134.5	134.5	134.5	100	Pass	9999	Pass
Pcon_c1=	4.6	W	4.6	4.6	4.6	3.9	Pass	22.7	Pass
Icon_%_c1=	117.9	%	117.9	117.9	117.9	100	Pass	9999	Pass
Pcon_c2=	8.1	W	8.1	8.1	8.1	6.8	Pass	22.7	Pass
Icon_%_c2=	122.1	%	122.1	122.1	122.1	100	Pass	9999	Pass
Pcon_c3=	18.5	W	18.5	18.5	18.5	14.2	Pass	22.7	Pass
Icon_%_c3=	134.6	%	134.6	134.6	134.6	100	Pass	9999	Pass
Pcon_c4=	34	W	34	34	34	28.7	Pass	38.9	Pass
Icon_%_c4=	117.5	%	117.5	117.5	117.5	100	Pass	9999	Pass
Type-2_Enable=	1	****	1	1	1	1	Pass	1	Pass
Test: pwr_on_maxi									
Ilim_Peak_1=	483	mA	483	483	483	0	Pass	1750	Pass
Ilim_Min_1=	403.6	mA	403.6	403.6	403.6	400	Pass	1750	Pass
Tlim_1=	61.3	msec	61.3	61.3	61.3	10	Pass	9999	Pass
Vlim_1=	55.2	V	55.2	55.2	55.2	50	Pass	57	Pass
Ilim_Max_1=	500.5	mA	500.5	500.5	500.5	400	Pass	1750	Pass
Ktran_lo_1=	110.3	%	110.3	110.3	110.3	92.4	Pass	115	Pass
Ilim_Peak_2=	849	mA	849	849	849	0	Pass	1750	Pass
Ilim_Min_2=	687	mA	687	687	687	683	Pass	1750	Pass
Tlim_2=	62.1	msec	62.1	62.1	62.1	10	Pass	75	Pass
Vlim_2=	54.8	V	54.8	54.8	54.8	50	Pass	57	Pass
Ilim_Max_2=	866.5	mA	866.5	866.5	866.5	683	Pass	1750	Pass
Ktran_lo_2=	109.4	%	109.4	109.4	109.4	92.4	Pass	115	Pass

Enhanced Test Condition

Test: pwrn_overld									
%Ipeak_1=	125	%	125	125	125	100	Pass	125	Pass
Vport_Ipeak_1=	55.4	V	55.4	55.4	55.4	50	Pass	57	Pass
Vport_5%DC_1=	55.4	V	55.4	55.4	55.4	50	Pass	57	Pass
%Ipeak_2=	116.9	%	116.9	116.9	116.9	100	Pass	125	Pass
Vport_Ipeak_2=	54.9	V	54.9	54.9	54.9	50	Pass	57	Pass
Vport_5%DC_2=	54.9	V	54.9	54.9	54.9	50	Pass	57	Pass
Test: mps_dc_valid									
Min_Valid_Time_Tmps=	10	msec	10	10	10	1	Pass	60	Pass
Duty_Cycle_tol=	1	****	1	1	1	1	Pass	1	Pass
Test: mps_dc_pwrn									
Min_Valid_I_hold=	6	mA	6	6	6	5	Pass	10	Pass
Time-to-Shutdown_Tmpdo=	366	msec	366	366	366	300	Pass	400	Pass
Test: pwrn_overld									
Icut_1=	377.5	mA	377.5	377.5	377.5	-1	Pass	1750	Pass
Tcut_1=	61.4	msec	61.4	61.4	61.4	50	Pass	9999	Pass
Isoft_1=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_1=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Icut_2=	640.5	mA	640.5	640.5	640.5	-1	Pass	1750	Pass
Tcut_2=	61.2	msec	61.2	61.2	61.2	10	Pass	9999	Pass
Isoft_2=	-1	mA	-1	-1	-1	-1	Pass	683	Pass
Tsoft_2=	-1	msec	-1	-1	-1	-1	Pass	2000	Pass
Test: pwrn_time									
Turn-Off_Time_Toff=	10.4	msec	10.4	10.4	10.4	0	Pass	500	Pass
Output_Cap_Cout=	0.0535	uF	0.0535	0.0535	0.05	-1	Pass	0.52	Pass
Output_Load_Rp=	85.1	Kohm	85.1	85.1	85	45	Pass	50000	Pass
Test: pwrn_v									
Avg_Idle_Voff=	0.2	V	0.2	0.2	0.2	0	Pass	2.8	Pass
Error_Delay_Ted=	1648.4	msec	1648.4	1648.4	1648	750	Pass	10000	Pass
Peak_Error_Delay_Ved=	1	V	1	1	1	0	Pass	20.5	Pass
Test Port Model Number:	3202								
Test Port Hardware Version:	11								
Test Port Firmware Version:	4.19 lc19								